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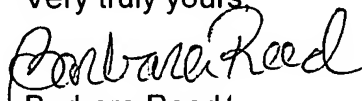
Re: Divisional Application of U.S. Serial No.: 09/074,078
Application of Kazuhiko ADACHI
For: **SEMICONDUCTOR DEVICE
HAVING A FLIP-CHIP CONSTRUCTION**
Our Docket No. 2271/56389-Z

Dear Ms. Inman:

Further to your telephone conference with me on September 8, 2004, and pursuant to your request, I enclose a photocopy of the originally submitted documents filed originally in the above-identified patent application on July 23, 2003, and the copy of the application contains no new matter.

If you have any questions or concerns, please do not hesitate to contact us.

Very truly yours,


Barbara Reed

Assistant to Ivan S. Kavrukov

bar/

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Application of : Kazuhiko ADACHI
Serial No. : Divisional of 09/074,078
Filed : Herewith
For : SEMICONDUCTOR DEVICE HAVING A FLIP-CHIP
CONSTRUCTION

Mail Stop Patent Application

Commissioner for Patents
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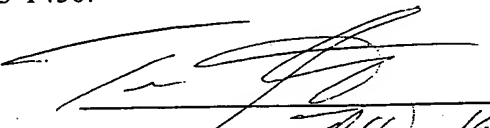
Sir:

**EXPRESS MAIL CERTIFICATE OF MAILING
FOR ABOVE-IDENTIFIED APPLICATION**

"Express Mail" mailing label number: EV325706398US

Date of Deposit: July 23, 2003

I hereby certify that this divisional application of Kazuhiko ADACHI, filed herewith, including a copy of prior application Serial No. 09/074,078, as filed on May 7, 1998, a copy of the Declaration and Power of Attorney filed in Serial No. 09/074,078, Preliminary Amendment, Information Disclosure Statement (including Form PTO-1449), Transmittal Letter in triplicate, a check in the amount of \$750.00, and return postcard, is being deposited with the United States Postal Service "Express Mail Post Office to Addressee" service under 37 C.F.R. §1.10 on the date indicated above and is addressed to Mail Stop Patent Application, Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.


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